

JC02 Rec'd PCT/PTO 25 MAY 2005

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Hiroji AGA et al.

Application No.: 10/522,413

Docket No.: 121797

Filed: January 26, 2005

For: METHOD OF FABRICATING SOI WAFER

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
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Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records in accordance with the Preliminary Amendment filed concurrently herewith.

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Respectfully submitted,

William P. Berridge
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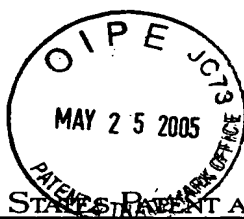
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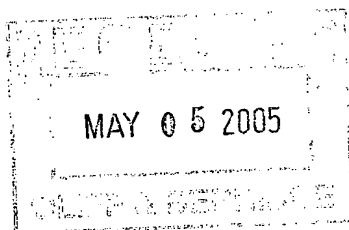


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/522,413	01/26/2005	2812	900	121797	8	8	1

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Applicant(s)

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Assignment For Published Patent Application

Shin-Etsu Handotai Co., Ltd., Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/09007 07/16/2003

Foreign Applications

JAPAN 2002-219308 07/29/2002

Projected Publication Date: 08/04/2005

Non-Publication Request: No

Early Publication Request: No

* PLEASE SEE PAGE 2 FOR CORRECTIONS TO THE TITLE *

Title

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~~Soi wafer manufacturing method~~

Preliminary Class

METHOD OF FABRICATING SOI WAFER

438

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